

	<b>L #</b>	<b>Hit</b>	<b>S arch T xt</b>	<b>DBs</b>
<b>1</b>	<b>L1</b>	<b>2065</b>	<b>(361/767,768,777,779,803).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>2</b>	<b>L2</b>	<b>1324</b>	<b>(174/262,263).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>3</b>	<b>L3</b>	<b>3148</b>	<b>1 2</b>	<b>USP AT; US-P GPU B</b>
<b>4</b>	<b>L4</b>	<b>156</b>	<b>3 and (castellation\$1 castellating notch\$2)</b>	<b>USP AT; US-P GPU B</b>
<b>5</b>	<b>L5</b>	<b>497</b>	<b>3 and (groove\$1 channel\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>6</b>	<b>L6</b>	<b>447</b>	<b>5 not 4</b>	<b>USP AT; US-P GPU B</b>